

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	2468189
<b>Application Number:</b>	10620151
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	8500
<b>Title of Invention:</b>	Digital clock recovery circuit employing fixed clock oscillator driving fractional delay line
<b>First Named Inventor/Applicant Name:</b>	Matthew A. Kliesner
<b>Customer Number:</b>	27975
<b>Filer:</b>	David L. Stewart/joellen murphy
<b>Filer Authorized By:</b>	David L. Stewart
<b>Attorney Docket Number:</b>	72206
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<b>Filing Date:</b>	15-JUL-2003
<b>Time Stamp:</b>	16:46:31
<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part/.zip	Pages (if appl.)
1		72206rfoa.pdf	475827 74e34b4dd04c74fb424f15dc0d1ffdb246 64eb0a43	yes	12

Multipart Description/PDF files in .zip description			
Document Description	Start	End	
Amendment After Final	1	1	
Claims	2	8	
Applicant Arguments/Remarks Made in an Amendment	9	12	

**Warnings:**

**Information:**

Total Files Size (in bytes):	475827
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**New Applications Under 35 U.S.C. 111**  
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**  
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